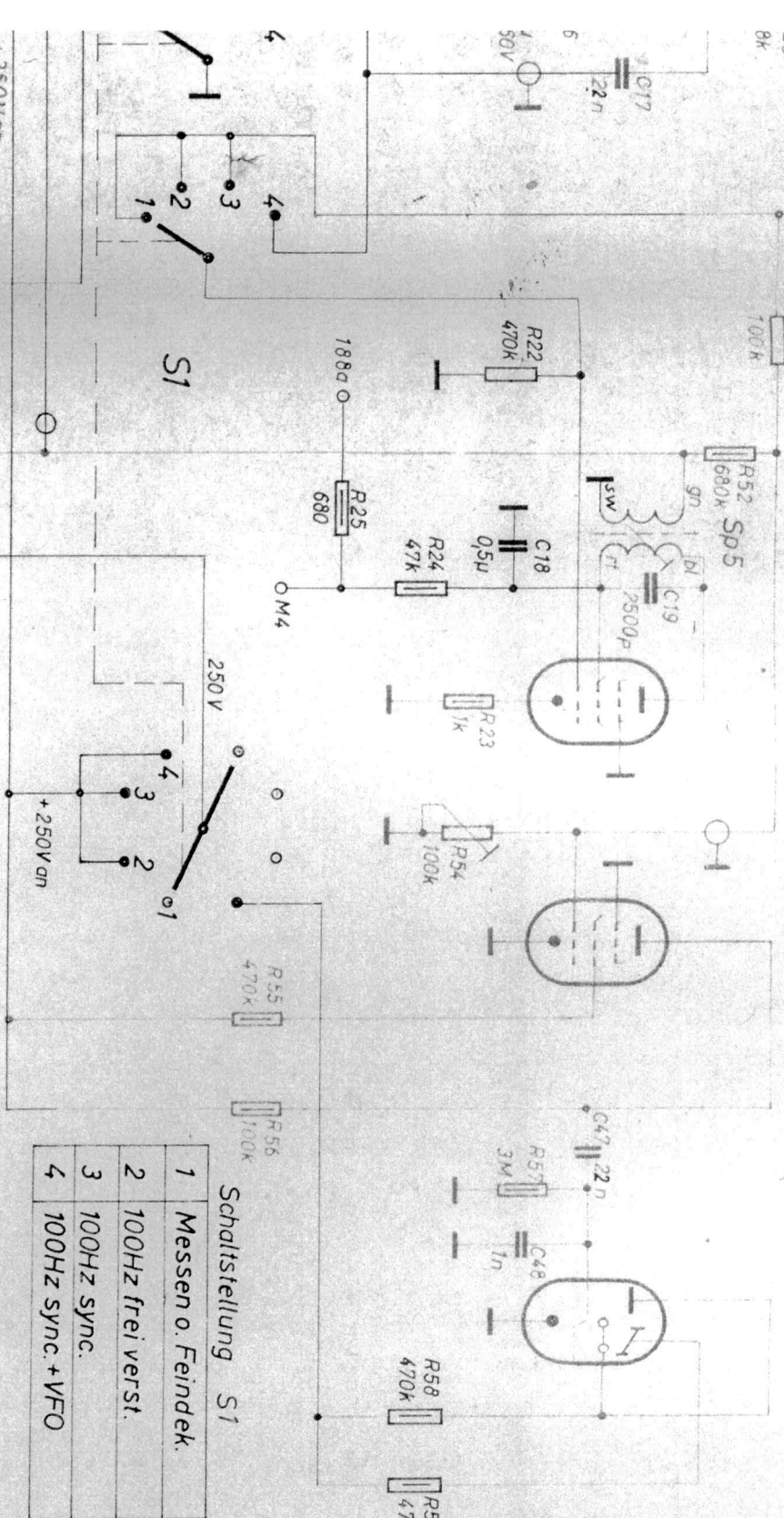
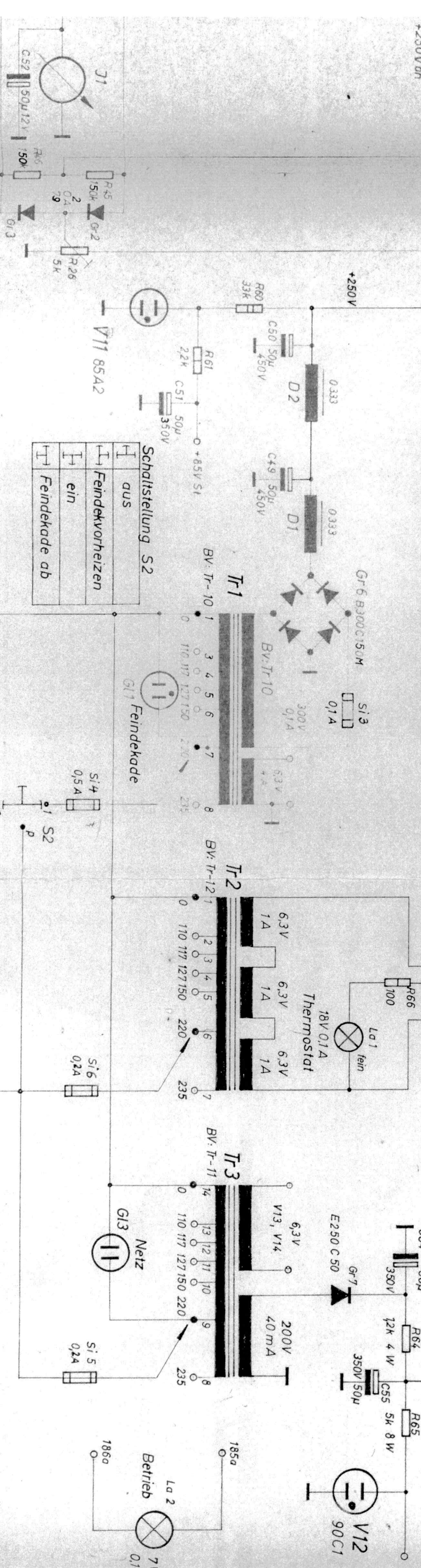
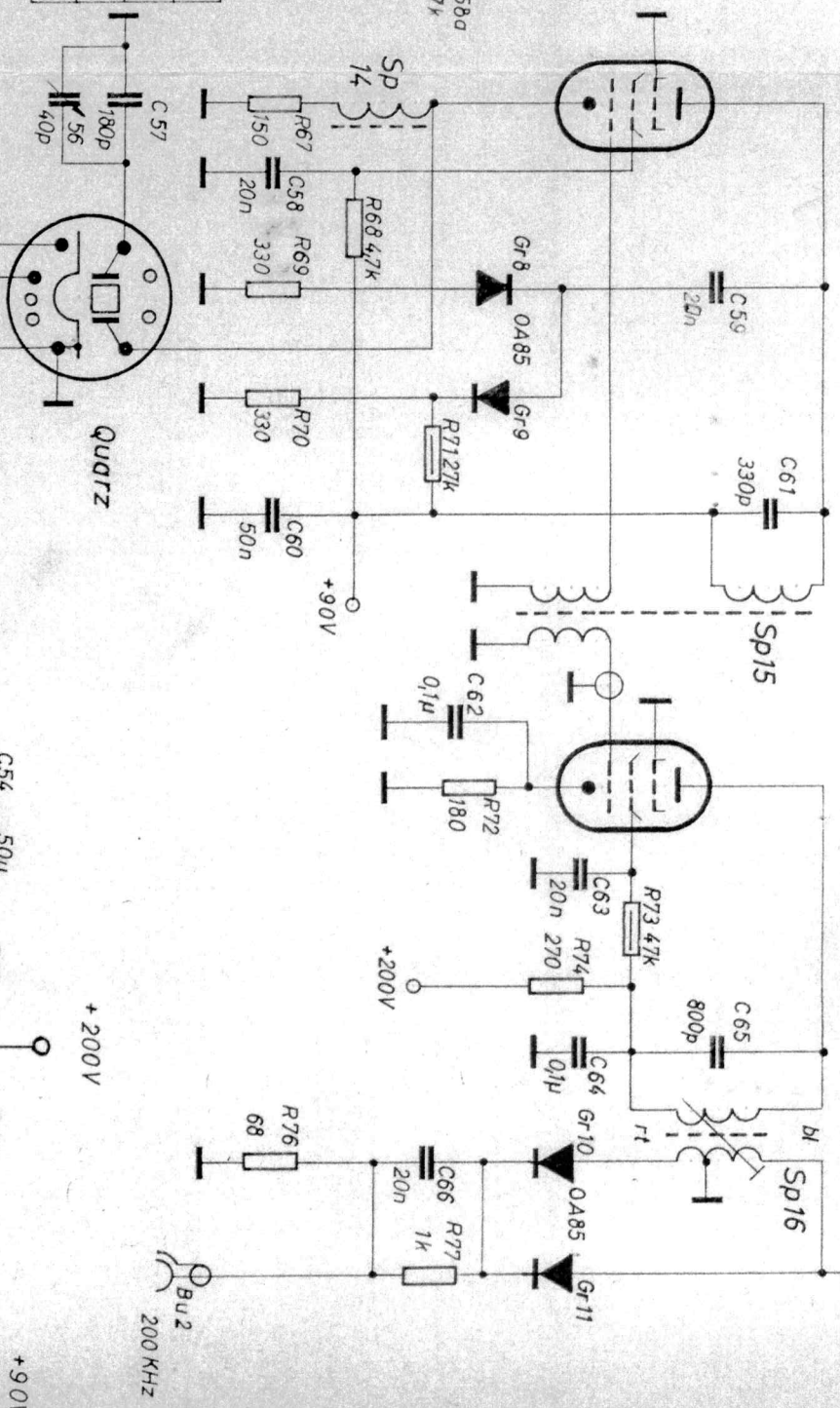
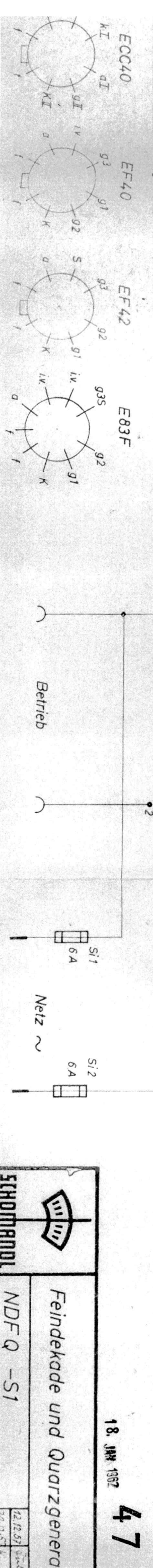




Schaltstellung S1	
1	Messen o. Feindek.
2	100Hz frei verst.
3	100Hz sync.
4	100Hz sync. + VFO



Schaltstellung S2	
1	aus
2	Feindekvorheizen
3	ein
4	Feindekade ab



Feindekade und Quarzgenerat.

NDF Q - S1

18. JAN 1962

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V6 EF40

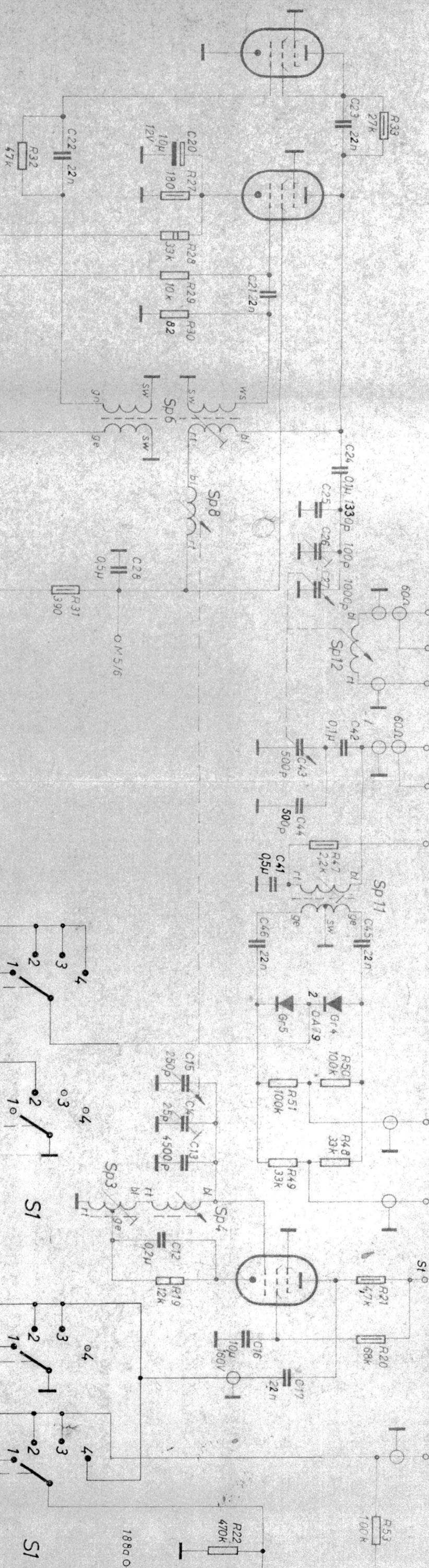
V5 EF42

II181 II182 II186
N.SP. +N.SP. Ph.Br. +Ph.Br.

I182 I181
Reg./I J/I

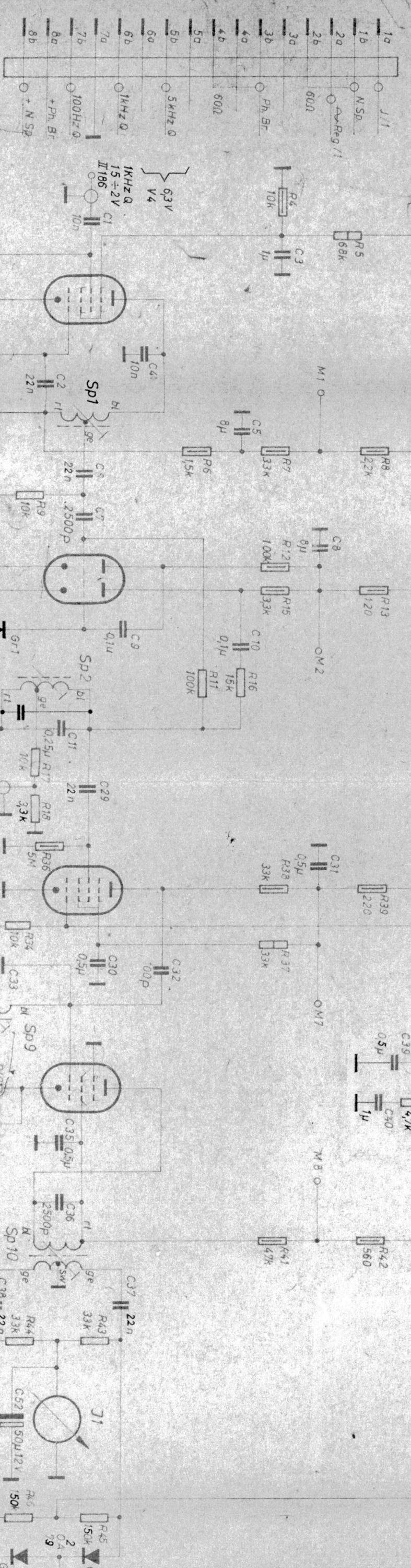
II185
5KHz

+85V V3 EF40



SI 180

+250V an



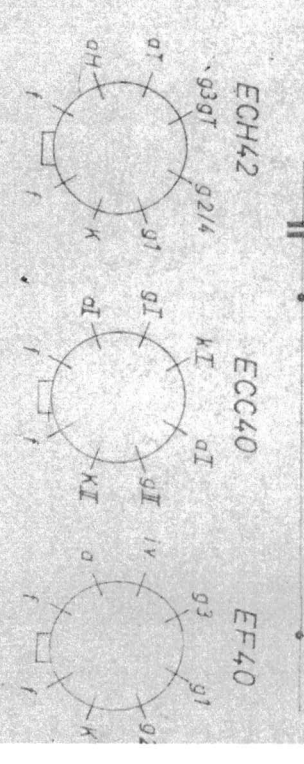
V1 ECH42

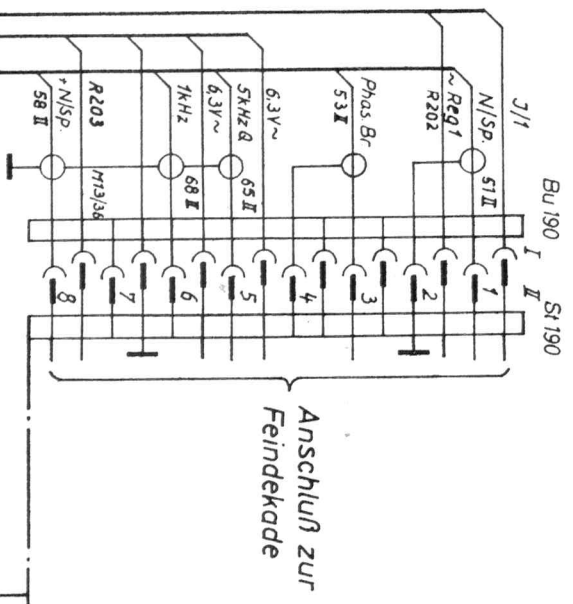
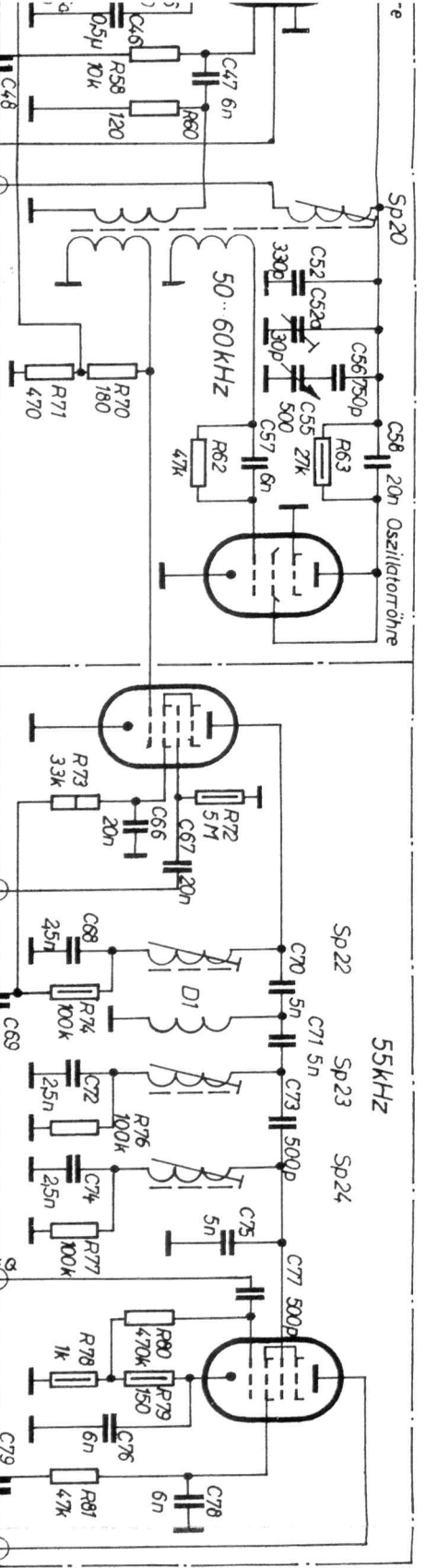
V2 ECC40

V7 ECH42

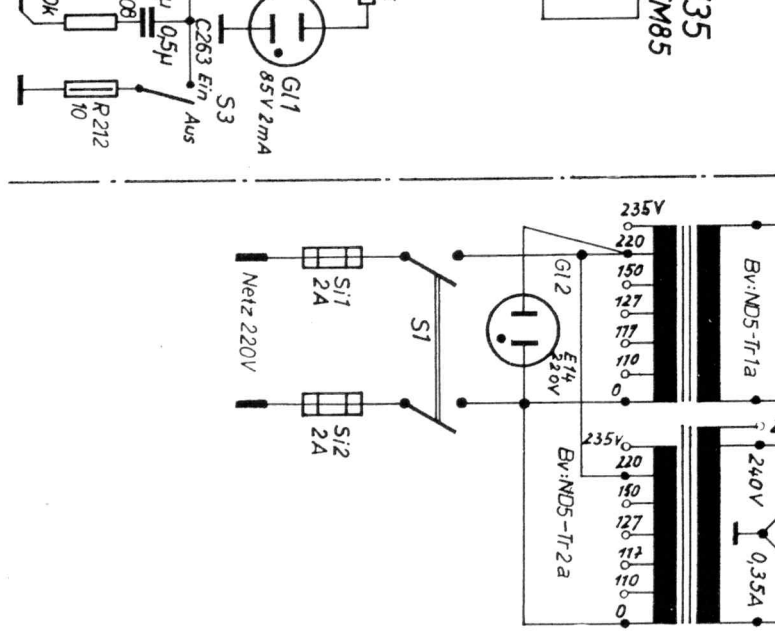
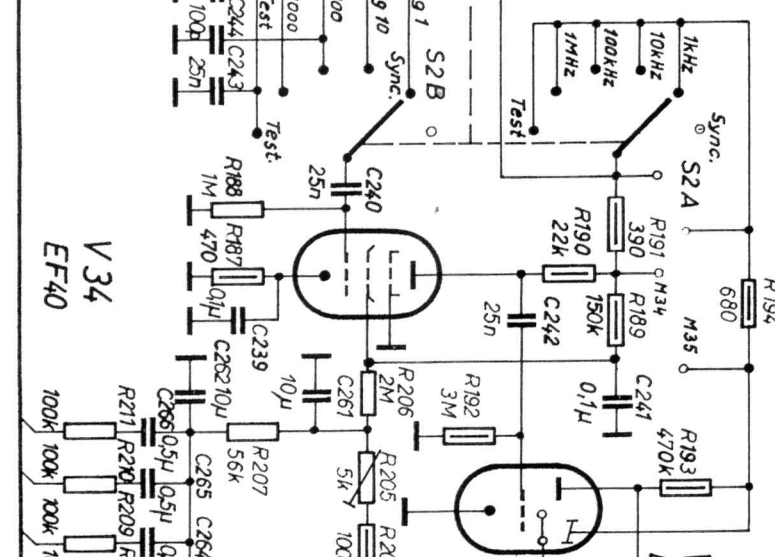
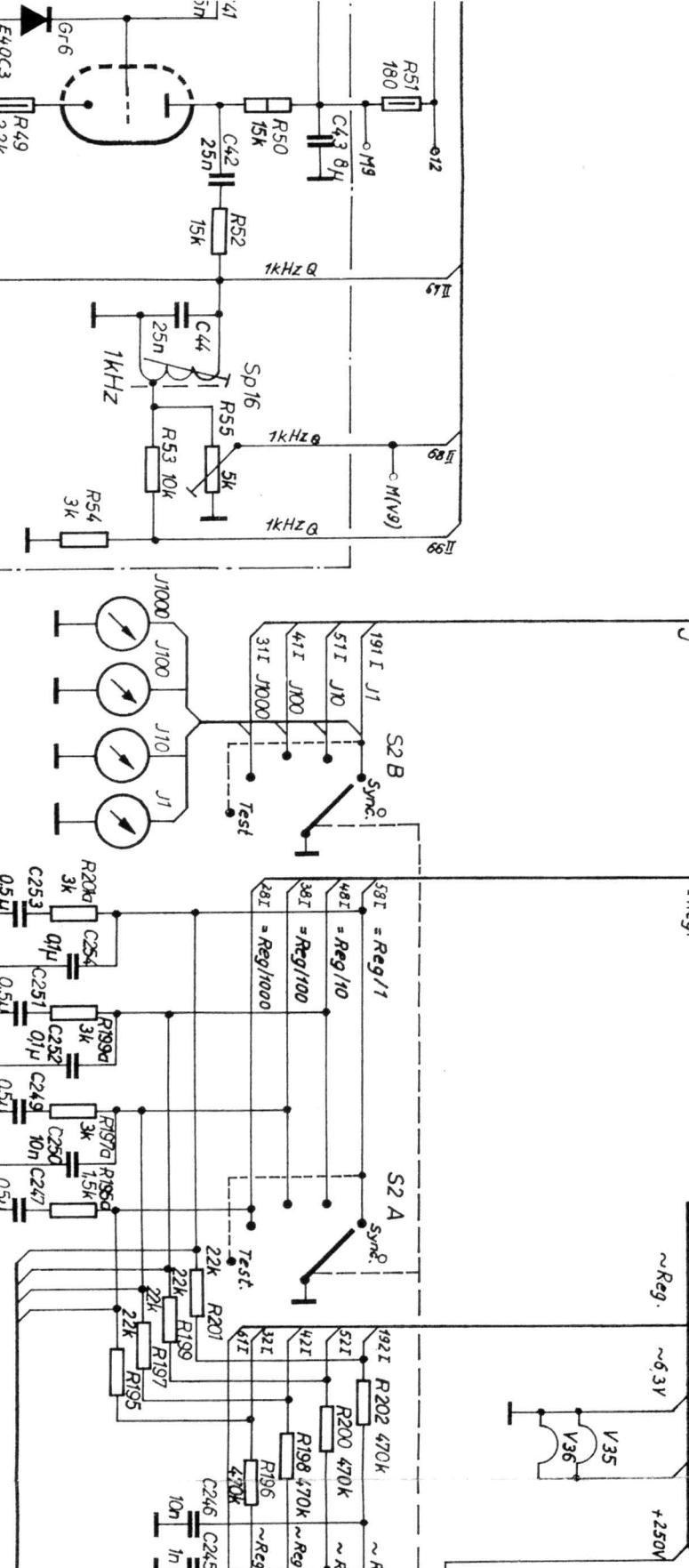
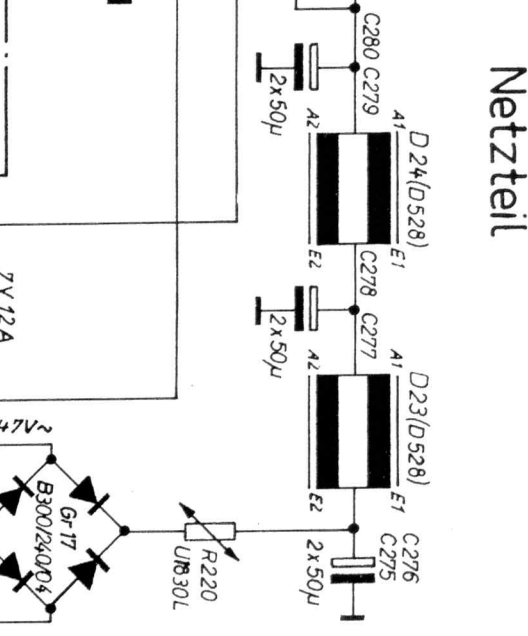
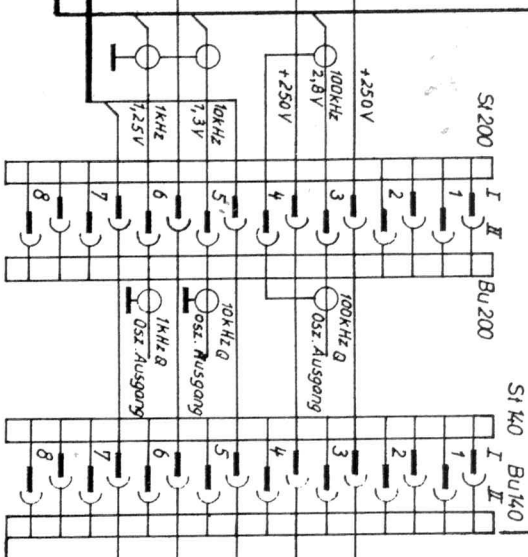
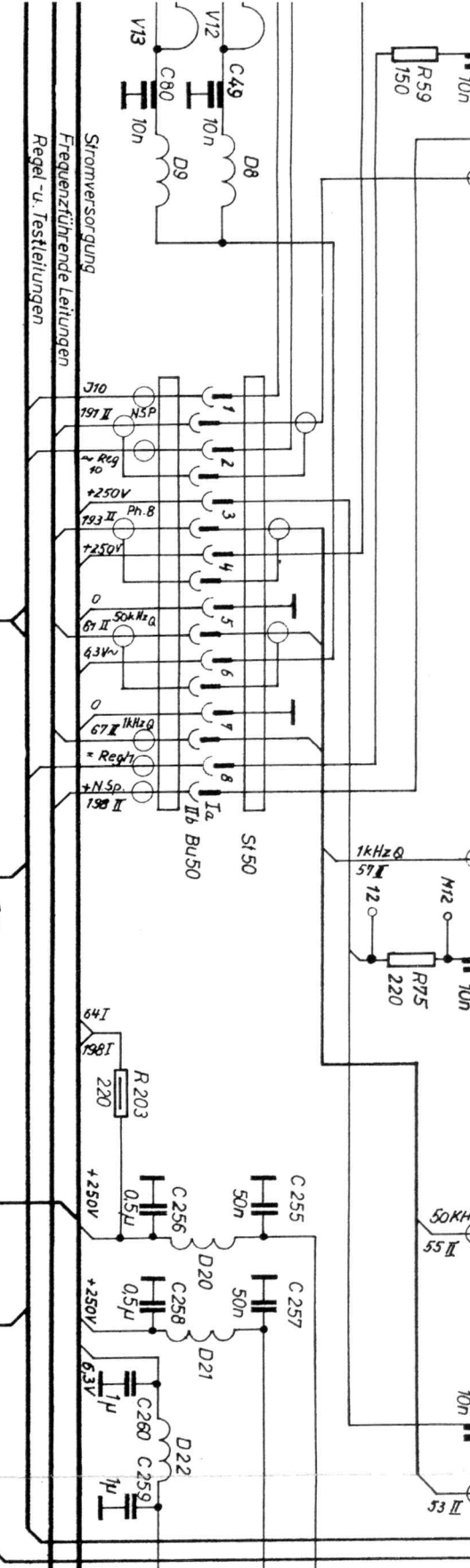
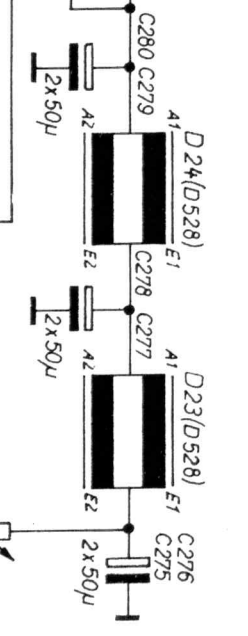
V8 EF40

100Hz Bu1 II187





Netzteil



V1 EF42

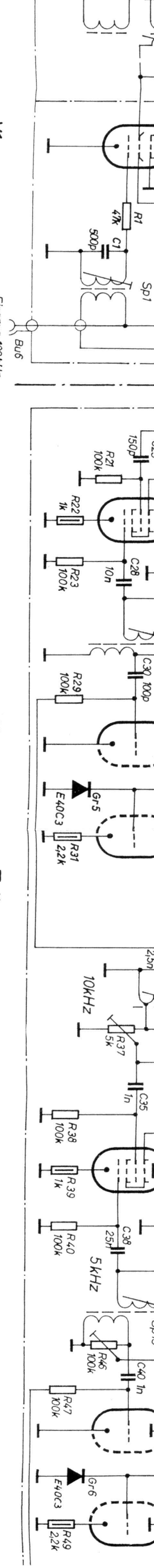
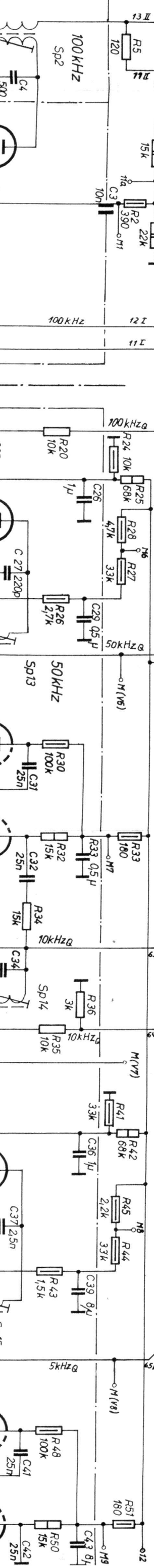
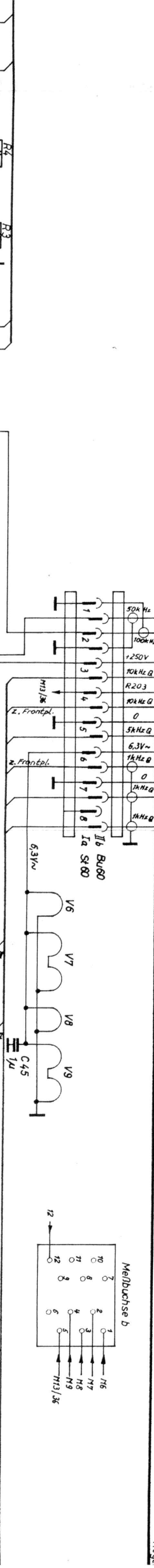
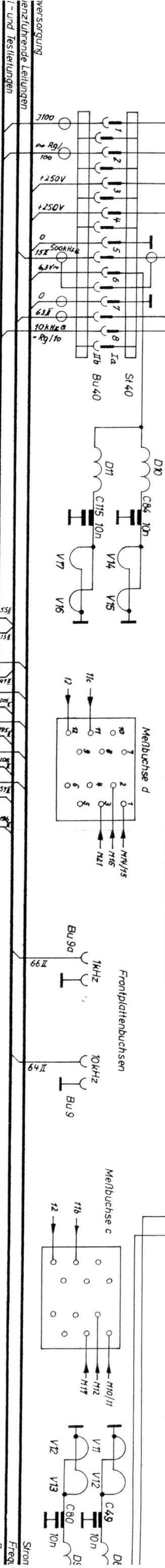
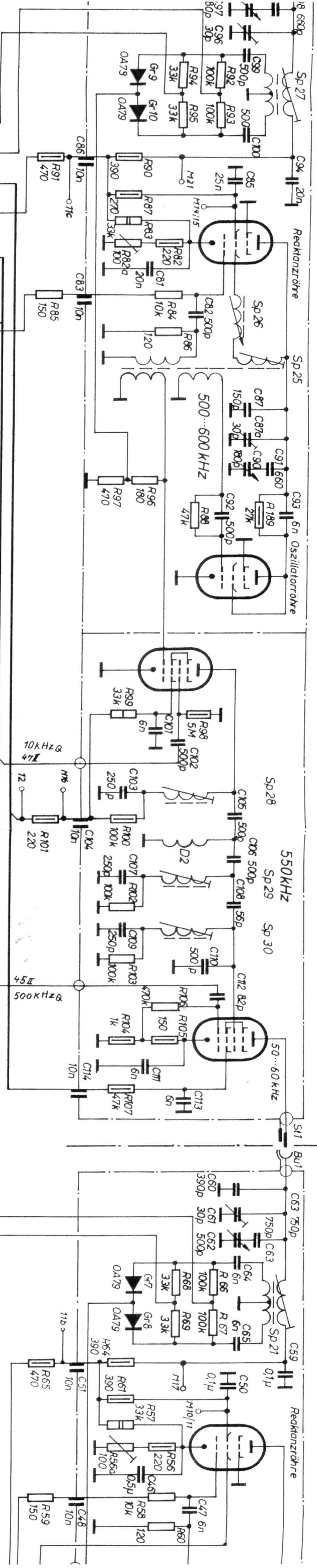
V15 EF40

V16 ECH42

10kHz-Stufe

V17 ECH42

V10 EF42



V1 EF40

V6 ECH42

V7 ECC40

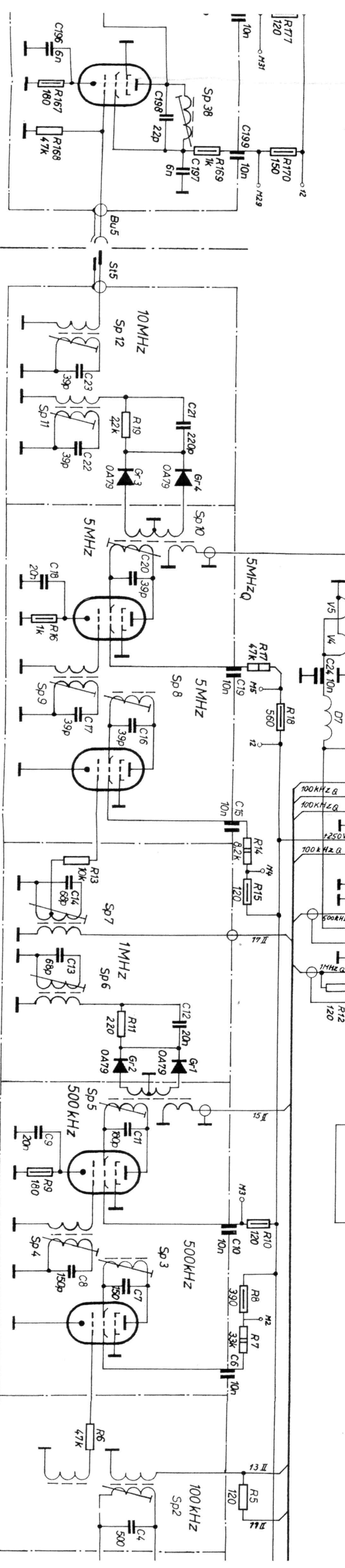
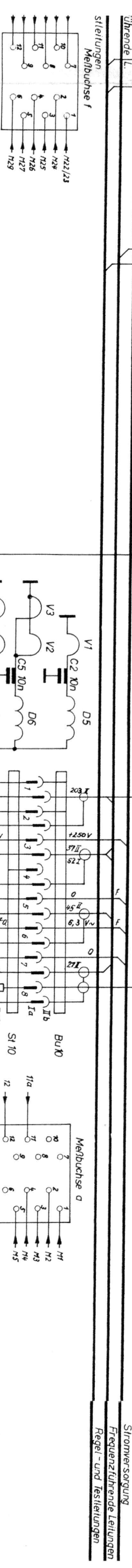
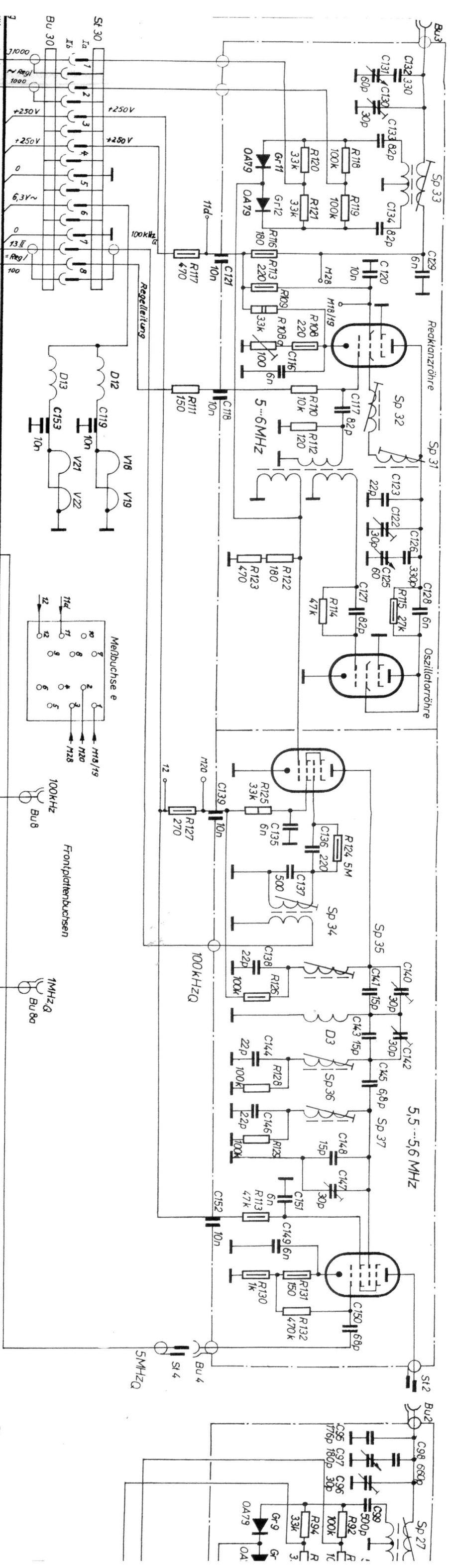
Teiler

V8 ECH42

V9 ECC40



V18 ^{EFF42}	100 kHz-Stufe	V19 ^{EFF40}	V20 ^{ECH42}	V21 ^{ECH42}
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V24 EF42

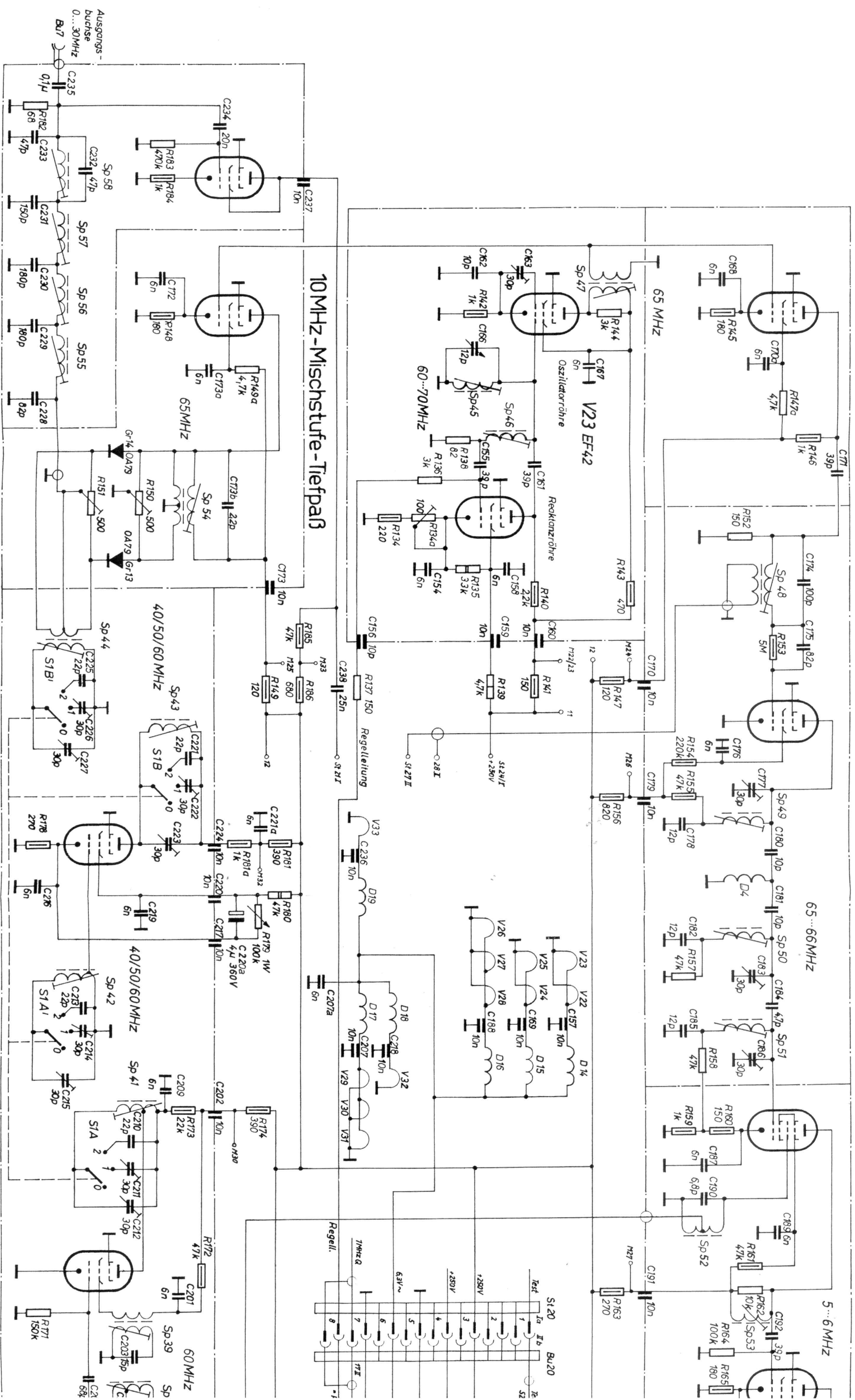
V22 EF42

V26 EF42

1MHz-Stufe

V27 ECH42

V28



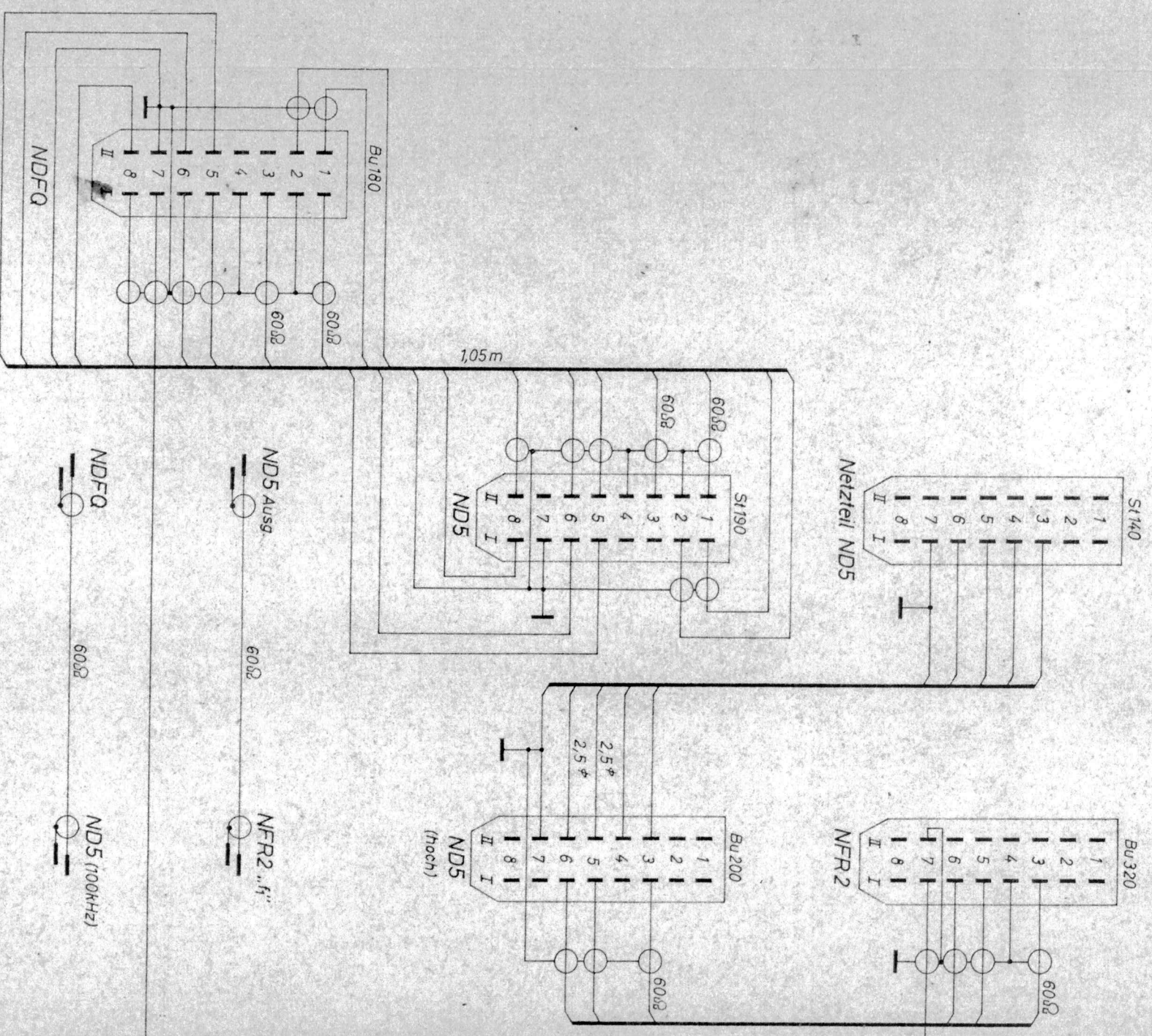
V33 EF40

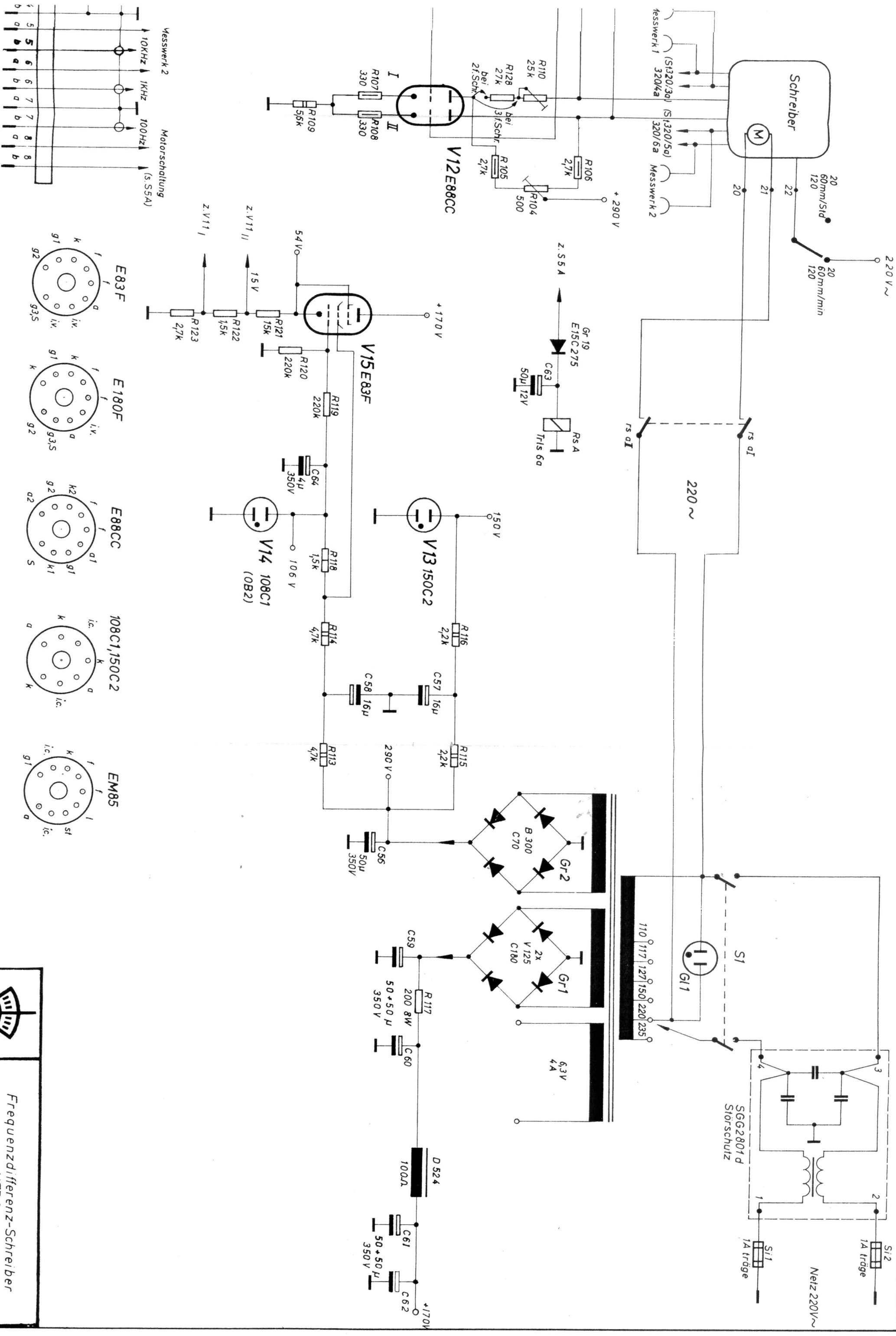
V25 EF42

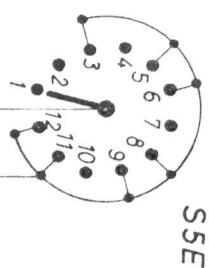
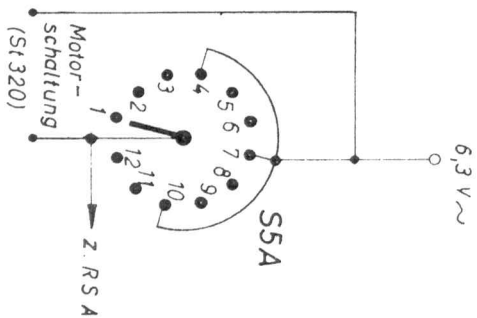
V32 EF42

Vervielfacher II

V30 EF42

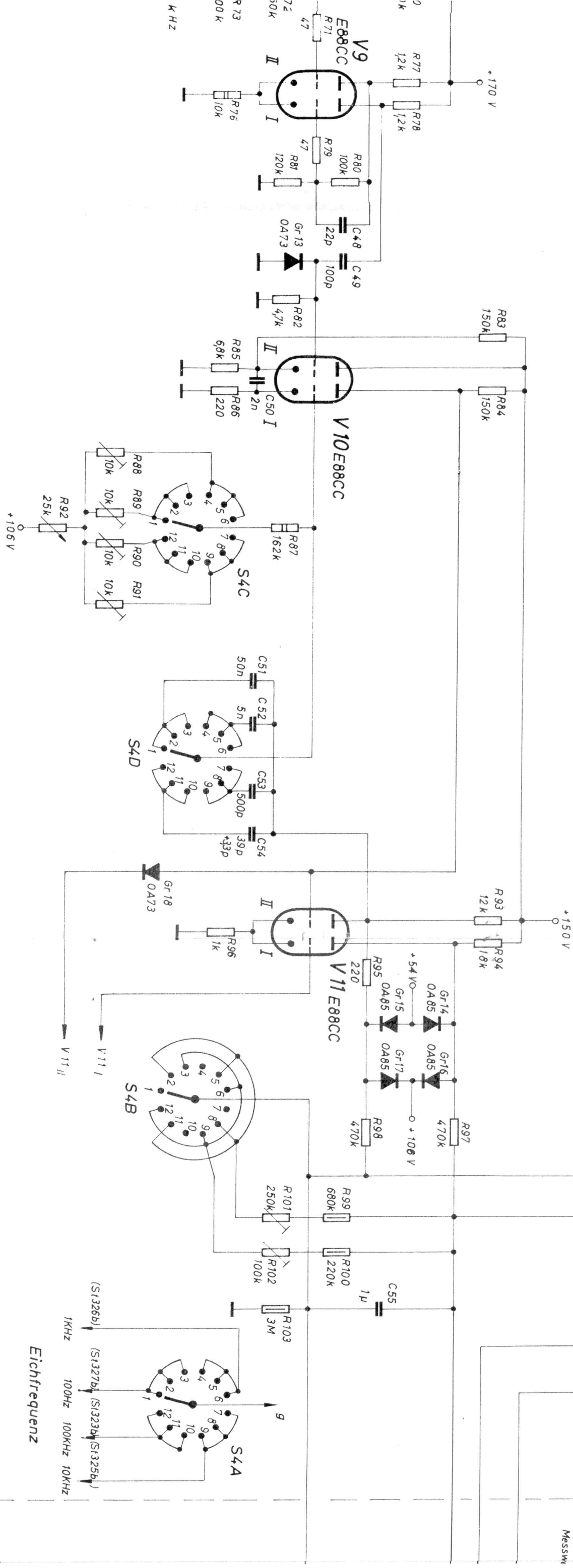
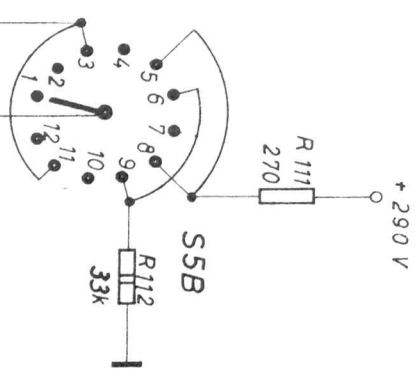






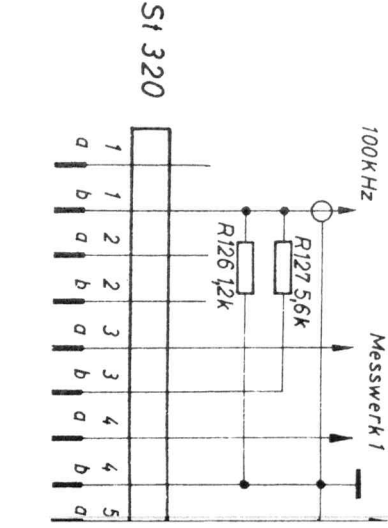
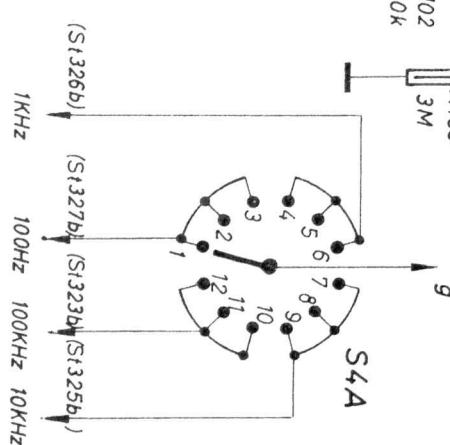
Schaltstellung S5

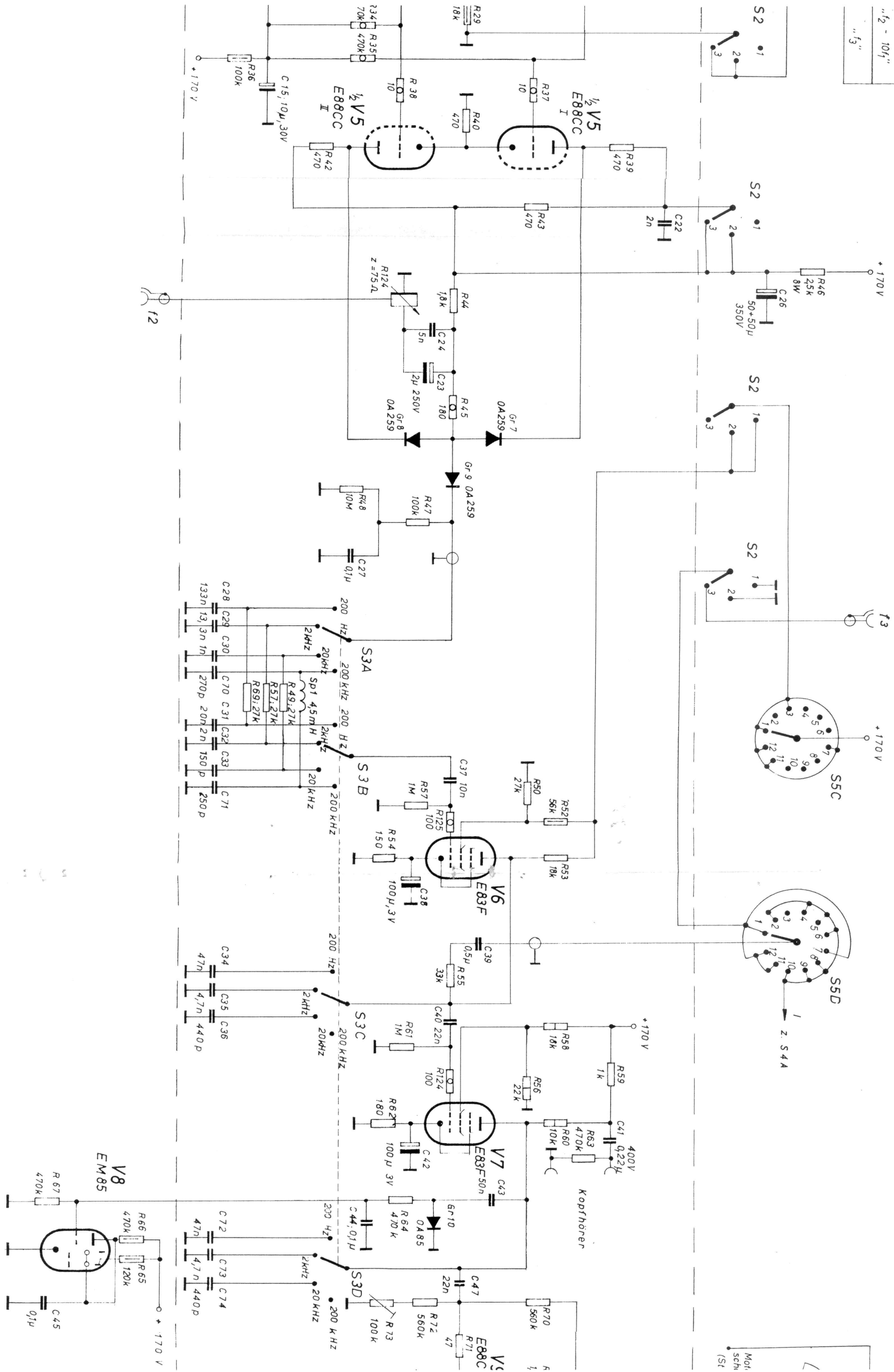
1		7	
2		8	
3	Instr., 0" Mech.	9	
4		10	
5		11	Instr., 0" Mech.
6		12	Instr., 0" EL.



Δf_0	Bereich	Δf_0	Bereich
1	100Hz $\pm$ 10Hz	7	10kHz $\pm$ 1kHz
2	100Hz $\pm$ 20Hz	8	10kHz $\pm$ 2kHz
3	100Hz $\pm$ 40Hz	9	10kHz $\pm$ 4kHz
4	1kHz $\pm$ 100Hz	10	100kHz $\pm$ 10kHz
5	1kHz $\pm$ 200Hz	11	100kHz $\pm$ 20kHz
6	1kHz $\pm$ 400Hz	12	100kHz $\pm$ 40kHz

Eichfrequenz



$$\begin{array}{l} \text{"}f_2 - f_1\text{"} \\ \text{"}f_2 - 10f_1\text{"} \\ \text{"}f_3\text{"} \end{array}$$


1	" $f_2 - f_1$ "
2	" $f_2 - 10f_1$ "
3	" f_3 "

Schaltstellung S2

